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2124

PTO/SB/21 (09-04)

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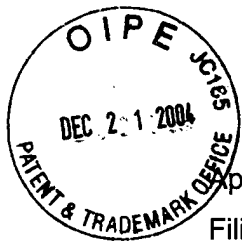
TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/976920
	Filing Date	10/15/2001
	First Named Inventor	CHARLES DOUGLAS MURPHY
	Art Unit	2124
	Examiner Name	
	Attorney Docket Number	CHAT C. DO
Total Number of Pages in This Submission		

ENCLOSURES (Check all that apply)		
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Application Number: 09/976920

Filing Date: 10/15/2001

Applicant: Charles Douglas Murphy

Title: SHARED MULTIPLICATION IN SIGNAL PROCESSING
TRANSFORMS

Examiner/GAU: Chat C. Do

December 19, 2004

AMENDMENT A

Mail Stop Non-Fee Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed September 23, 2004, Applicant (a *pro se* filer) amends the above application as follows:

Specification:

In the section DRAWING FIGURES on page 10, lines 10-11 have been modified as follows:

Fig 2 shows the 16-bit twos' complement representations of $\sin(2 \pi / N)$ and $\sin(4 \pi / N)$ for $N = 64$.

Fig 3 shows an embodiment of the invention wherein desired products which are the imaginary components of weighted real inputs to a 64-point DFT are computed and added to sums of products which are the DFT outputs, including one multiplier which does not use intermediate terms of other desired product computations and one multiplier which does use intermediate terms of another desired product computation.